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APPLICANTS

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** CONTINUING DATA *****

This application is a 371 of PCT/GB00/02321 06/15/2000 *Dml*

** FOREIGN APPLICATIONS *****

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Verified and Acknowledged Examiner's Signature	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Dml</i> Initials				<i>9</i>

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TITLE

Semiconductor device simulation method and simulator

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